

This listing of claims will replace all prior versions, and listings, of claims in the application:

**The Status of the Claims**

1. (Currently Amended) A write current circuit for a mass media write head, comprising:
  - a head write driver circuit adapted to drive the write head with a write current signal having a positive write edge and a negative write edge; and
  - a circuit coupled with the head write driver circuit and adapted to selectively provide pulsing signals which independently define ~~an overshoot amplitude~~ overshoot amplitudes of said positive write edge and said negative write edge respectively of said write current signal.
2. (Original) The write current circuit of claim 1, wherein said further circuit is a differential current source.
3. (Original) The write current circuit of claim 2, wherein said differential current source is programmable.
4. (Previously Presented) The write current circuit of claim 1, wherein said further circuit is adapted to selectively provide a defined amplitude.
5. (Original) The write current circuit of claim 4, wherein said further circuit is programmable for providing differential overshoot amplitudes for said positive write edge and said negative write edge.

6. (Original) The write current circuit of claim 1, wherein said further circuit includes a delay circuit for selectively providing a defined pulse width for each of said overshoots.

7. (Original) The write current circuit of claim 6, wherein said delay circuit is programmable for providing differential overshoot pulse widths for said positive write edge and said negative write edge.

8. (Original) The write current circuit of claim 1, wherein said further circuit is adapted to selectively provide a defined amplitude of each of said overshoots and includes a delay circuit for providing a defined pulse width for each of said overshoots.

9. (Original) The write current circuit of claim 8, wherein said further circuit and said delay circuit are programmable for providing differential overshoot amplitudes and pulse widths for said positive write edge and said negative write edge.

10. (Currently Amended) A write driver for an inductive head element in a disk drive system, said driver comprising:

an H-bridge circuit capable of driving a first current and a second current through said head element;

a boost circuit coupled with said H-bridge and operable for delivering a current pulse during time periods defining a positive edge of said first current and a negative edge of said first current responsive to a control signal, wherein a sum of said first current and said second current provides the write current for said head element; and

said boost circuit is further adapted to selectively vary said positive edge current pulse and said negative edge current pulse; and

a circuit coupled with the head write driver circuit and adapted to selectively provide pulsing signals which independently define ~~an overshoot amplitude~~ overshoot amplitudes of said positive write edge and said negative write edge respectively of said write current signal.

11. (Original) The write driver of claim 10, wherein said boost circuit includes a programmable differential current source.

12. (Original) The write driver of claim 10, wherein said boost circuit is further adapted to selectively provide a defined amplitude for each of said positive edge current pulse and said negative edge current pulse.

13. (Original) The write driver of claim 12, wherein said boost circuit is programmable for providing differential amplitudes for said positive edge current pulse and said negative edge current pulse.

14. (Original) The write driver of claim 10, wherein said boost circuit further includes a delay circuit for selectively providing a defined pulse width for each of said positive edge current pulse and said negative edge current pulse.

15. (Original) The write driver of claim 14, wherein said delay circuit is programmable for providing differential pulse widths for said positive edge current pulse and said negative edge current pulse.

16. (Original) The write driver of claim 10, wherein said boost circuit is further adapted to selectively provide a defined amplitude for each of said positive edge current pulse and said negative edge current pulse and further includes a delay circuit for selectively providing a defined pulse width for each of said positive edge current pulse and said negative edge current pulse.

17. (Original) The write driver of claim 16, wherein said boost circuit and said delay circuit are programmable for providing differential amplitudes and pulse widths for said positive edge current pulse and said negative edge current pulse.

18. (Currently Amended) A method of providing a write current to an inductive head element in a disk drive system, comprising:

providing current pulses cooperable for independently defining a positive edge and a negative edge of said write current;

differentially varying ~~an amplitude~~ respective amplitudes of said positive edge current pulse and said negative edge current pulse for counteracting induced imbalances in said write current.

19. (Currently amended) The method of ~~claim 19~~ claim 18 further comprising varying a pulse width of said positive edge current pulse and said negative edge current pulse for further counteracting induced imbalances corresponding to said disk drive system.